

ABSTRACT:

A voltage level detection circuit (1) with a threshold level which is dependent on the manufacturing process. The circuit comprises a first current generator (4) which generates a monitoring current (I_M) derived from the voltage (V_M) to be monitored. This monitoring current (I_M) is compared with a reference current (I_{ref1}). A switchable reference current (I_{ref2}) provides for hysteresis.

The first current generator (4) comprises an element, the resistance of which depends on the manufacturing process.

Figure 1.

Figure 1 is a schematic diagram of a voltage level detection circuit (1). The circuit includes a first current generator (4) which generates a monitoring current (I_M) derived from the voltage (V_M) to be monitored. This monitoring current (I_M) is compared with a reference current (I_{ref1}). A switchable reference current (I_{ref2}) provides for hysteresis. The first current generator (4) comprises an element, the resistance of which depends on the manufacturing process.